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Amendments to the Specification:

Please replace the paragraph beginning on page 10, line 15 with the following amended paragraph:

In the <u>display</u> ~~driving~~ section 11, an N number of gate lines G1, G2,··· , Gn, ··· , GN (hereinafter, the gate lines are referred to as gate lines G when collectively referred to), which are connected to the gate driver 12, and an M number of source lines S1, S2, ···, Sn, ···, SM (hereinafter, the source lines are referred to as source lines S when collectively referred to) are so arrayed as to cross each other perpendicularly. At each intersection of each line, a pixel PIX is connected. Each pixel PIX is connected with a liquid crystal capacitance, for example, via a TFT (Thin, Film Transistor) as a switching element. In short, the display section 11 is an active matrix-type liquid display panel in which the pixels PIX are positioned in matrix and are driven independently and respectively.

Please replace the paragraph beginning on page 12, line 11 with the following amended paragraph:

The data to be stored in the first memory 24 and the second memory 25 is supplied via a <u>bus</u> ~~bas~~ 26 connected to an external image signal source (not shown). The control section 23 controls the storing and reading of the data into/out of the first memory 24 and the second memory 25.

Please replace the paragraph beginning on page 19, line 14 with then following amended paragraph:

On the other hand, as step S14, if the n frames have <u>not</u> passed, the display operation goes to step S11 again, and it is judged whether the update date is present or absent.

Please replace the paragraph beginning on page 21, line 9 with the following amended paragraph:

Thus, for preventing the sticking of image, it is conceivable that the A area is moved to <u>a</u> ~~an~~ predetermined position on the B area in the display section 11. For example, as shown in

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Figure 7, the A area is scrolled line by line in the arrow direction on the display screen of the display section 11, that is, from an upper part to a lower part, or from the lower part to the upper part (up and down). Further it is also conceivable that the A area is randomly moved on the display screen of the display section 11 (hereinafter, this kind of display is referred to as random display), as shown in Figure 8.

Please replace the paragraph beginning on page 24, line 21 with the following amended paragraph:

The control section 23 controls the reading-out of the display data from the first memory 24 and the second memory 25, in accordance with a memory reading signal and a memory selecting signal. The memory reading signal indicates ~~indicate~~ whether the reading-out of the display data from the first memory 24 and the second memory 25 is to be performed or not. The memory selecting signal is for selecting from which one of the first memory 24 and the second memory 25 the display data is to be read out.

Please replace the paragraph beginning on page 26, line 16 with the following amended paragraph:

As described above, the A area and the B area are not overwritten similarly, that is, an ~~the a~~ overwriting period (an interval between overwriting process and next overwriting process) of the B area that is mainly for displaying the still image is longer than an [[a]] overwriting period of the A area in the display section 11. With this arrangement, it is possible to reduce the power consumption of the display section 11. Note that, a holding period of the display data may not be 3 frames, as long as the deterioration of the display image is below tolerance level. The longer the holding period of the display data, the lower power consumption.

Please replace the paragraph beginning on page 33, line 21 with the following amended paragraph:

In this arrangement, usually, a single color such as black or white is written batchwise in the B area, in many cases. Thus, the border between the A area and the B area becomes more

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visible. Thus, it is quite ~~quit~~ effective that the black or white line, which is different from the color of the B area, is written on the border between the A area and the B area.